

**Digital multiple axis controller for real-time process e.g. motion**

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**Inventor:** BRUNE RICHARD DIPL ING (DE); HOEPPNER BERNHARD DIPL ING (DE); ROCHHOLZ GUENTER DIPL ING (DE); WAGENPFEIL ALEXANDER DIPL ING (DE)  
**Applicant:** SIEMENS AG (DE)  
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The controller includes a central processor (CPU1) and at least one electric drive arrangement. Each electric drive includes an associated decentralized processor (CPU2, CPU<sub>n</sub>), whereby all decentralized processors are synchronized with the central processor over a programmable clock. All system conditions and measurements for the central and all decentralized processors, which are required for the control of the real-time process are stored (RA, RB, RC) at equidistant, synchronous points in time in a clock raster of the respective processor, in such way, that the associated system conditions and measurements are independently accessible at any time by the respective processor.

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